

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	29	(frame ring spac\$3 damp interposer) with (chip ic semiconductor cis cmos) and (clear transparen\$2 glass) with (substrate board cap housing cover\$3 lid plate) and (atmosphere pressure vacuum) with (vacuum low negative) and (frame ring spac\$3 damp interposer) with (hermetic sealant seal\$3) same ultraviolet	USPAT; EPO; JPO	OR	ON	2006/05/05 11:43
L4	11	("5501003"   "5516727"   "5821692"   "5866942"   "6071761"   "6232150"   "6297072"   "6500694"   "6624003"   "6627814"   "6660564").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/05 11:45
L10	11	("20020062787"   "4375018"   "4729640"   "4824073"   "4911968"   "5146671"   "5340978"   "5629787"   "5751492"   "6122009"   "6304243").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/05 11:56
L12	1	"6063646".pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/05/05 14:08
S60	6	(frame ring spac\$3 damp interposer) near4 (chip ic semiconductor cis cmos) with active and (clear transparen\$2 glass) with (substrate board cap housing cover\$3 lid plate) and (atmosphere pressure) with (vacuun under low negative) and (frame ring spac\$3 damp interposer) with (crack\$3 break\$3 broken collaps\$3)	USPAT; EPO; JPO	OR	ON	2006/05/04 12:28
S61	9	(frame ring spac\$3 damp interposer) near4 (chip ic semiconductor cis cmos) with active and (clear transparen\$2 glass) with (substrate board cap housing cover\$3 lid plate) and (atmosphere pressure) with (vacuun under low negative) and (frame ring spac\$3 damp interposer) with (sealant seal\$3)	USPAT; EPO; JPO	OR	ON	2006/05/05 11:41

## EAST Search History

S62	17	("4626960"   "4786357"   "4888449"   "4910584"   "5018004"   "5034044"   "5304453"   "5436407"   "5491110"   "5504026"   "5604160"   "5798557"   "5847280"   "5852320"   "5886457"   "5903437"   "6027630").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/04 12:30
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